

AUG 08 2007

S/N 10/521908

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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|-------------|---|-----------------|----------------|
| Applicant: | TSUKADA | Examiner: | HOANG, TU BA |
| Serial No.: | 10/521908 | Group Art Unit: | 2832 |
| Filed: | January 20, 2005 | Docket No.: | 10921.0269USWO |
| Title: | CHIP RESISTOR AND MANUFACTURING METHOD THEREFOR | | |

CERTIFICATE UNDER 37 CFR 1.6(d)

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on August 8, 2007.

By: 

Name: Laura Sindt

PETITION FOR EXTENSION OF TIME

Mail Stop: AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Dear Commissioner:

In accordance with the provisions of 37 C.F.R. §1.136(a), it is respectfully requested that a 3-month extension of time be granted in which to respond to the outstanding Office Action mailed February 8, 2007, said period of response being extended from May 8, 2007 to August 8, 2007.

Please charge Deposit Account No. 50-3478 in the amount of \$1,020 to cover the required extension fee for a large entity.

Respectfully submitted,

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PATENT TRADEMARK OFFICE

Dated: August 8, 2007

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